

**Notice of References Cited**Application/Control No.  
09/577,861Applicant(s)/Patent Under  
Reexamination  
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2185

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,281,721	08-2001	Kinget et al.	327/115
	B	US-6,229,367	05-2001	Choudhury, Ashish K.	327/276
	C	US-6,128,747	10-2000	Thoulon, Pierre-Yves	713/330
	D	US-6,025,745	02-2000	Lee et al.	327/277
	E	US-5,670,904	09-1997	Moloney et al.	327/277
	F	US-5,454,114	09-1995	Yach et al.	713/330
	G	US-5,049,766	09-1991	van Driest et al.	327/269
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM Technical Disclosure Bulletin, Vol. 37, No. 8, August 1994, "Programmable Delay Line Control Signal Circuits".
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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